

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Noriaki Chiba ) Group Art Unit  
Serial No. : New U.S. National Stage )  
Application )  
Filed : International Filing Date )  
December 12, 2003 )  
For : TIMING GENERATION CIRCUIT )  
AND SEMICONDUCTOR TEST )  
DEVICE HAVING THE TIMING )  
GENERATION CIRCUIT )  
Examiner : Unknown )

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INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner  
of Patents and Trademarks  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is a form PTO-1449 listing four references relevant to the above-identified application. A copy of the references is also enclosed herewith.

This information disclosure statement is filed at the time of filing the instant application concurrently herewith, therefore, no fee is required under 37 CFR 1.97(b)(1).

All of the references are cited in the International Search Report issued by Japanese Patent Office in the international stage of this case. An English version of the International Search Report is also enclosed. For the prior art references that are not

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in English, an English abstract is attached to each of the references when it is readily available.

Applicant respectfully requests that the prior art references submitted be considered in the substantive examination of this application.


Respectfully submitted,

MURAMATSU & ASSOCIATES

Dated: \_\_\_\_\_

6/9/05

By: \_\_\_\_\_

  
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JCOB Rec'd PCT/PTO 0'9 JUN 2005

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HEIWA.026AUS
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